

A Handheld High-Sensitivity Micro-NMR CMOS Platform With B-Field Stabilization for Multi-Type Biological/Chemical Assays

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Abstract—We report a micro-nuclear magnetic resonance (NMR) system compatible with multi-type biological/chemical lab-on-a-chip assays. Unified in a handheld scale (dimension: $14 \times 6 \times 11 \text{ cm}^3$, weight: 1.4 kg), the system is capable to detect $<100 \text{ pM}$ of *Enterococcus faecalis* derived DNA from a $2.5 \text{ }\mu\text{L}$ sample. The key components are a portable magnet (0.46 T, 1.25 kg) for nucleus magnetization, a system PCB for I/O interface, an FPGA for system control, a current driver for trimming the magnetic (B) field, and a silicon chip fabricated in $0.18 \text{ }\mu\text{m}$ CMOS. The latter, integrated with a current-mode vertical Hall sensor and a low-noise readout circuit, facilitates closed-loop B-field stabilization ($2 \text{ mT} \rightarrow 0.15 \text{ mT}$), which otherwise fluctuates with temperature or sample displacement. Together with a dynamic-B-field transceiver with a planar coil for micro-NMR assay and thermal control, the system demonstrates: 1) selective biological target pinpointing; 2) protein state analysis; and 3) solvent-polymer dynamics, suitable for healthcare, food and colloidal applications, respectively. Compared to a commercial NMR-assay product (Bruker mq-20), this platform greatly reduces the sample consumption ($120\times$), hardware volume ($175\times$), and weight ($96\times$).

Index Terms—Baseband, biological, biosensor, chemical, CMOS, deoxyribonucleic acid (DNA), filter, Hall sensor, immunoglobulin, lab-on-a-chip, low-noise amplifier (LNA), magnetic sensing, nuclear magnetic resonance (NMR), point-of-care, polymer, power amplifier (PA), protein, radio frequency (RF), receiver (RX), transceiver (TRX), transimpedance amplifier (TIA), transmitter (TX).

I. INTRODUCTION

Point-of-use (PoU) biological/chemical assays offer a prospect to modernize the bulky laboratory instruments into easy-to-use lab-on-a-chip platforms, bringing down the cost, size, and sample use by orders of magnitude [1].

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Beyond them, it is also tantalizing to develop a unified PoU platform apt for different application-specific assays [2]. Particularly for biomolecule target detection, the capability of the assay to sense the biomolecules specifically and selectively facilitates early disease detection [3]. From the report of the World Health Organization (WHO), almost 1/3 of reported death in low-income countries in 2012 was attributed to infectious diseases (i.e., HIV/AIDS, Malaria, and Tuberculosis) [4]. However, conventional centralized detection techniques (e.g., qualitative polymerase chain reaction and enzyme-linked immunosorbent assay) can have a long turn-around time and demand skillful operators. These restrictions perplexed rapid detection of contagious diseases and made it a Herculean task for governing the fierce spreading of the epidemic in rural and resource-limited regions.

Scientists and engineers have been intensively investigating multifarious of *in vitro* biosensors for rapid disease diagnosis [5]. For instance, the prevailing lateral flow assay is notable for its simplicity and inexpensiveness [6], yet its qualitative or semi-quantitative results limit their usages to only preliminary assays [7]. On the other hand, CMOS chips are promising for rapid and quantitative detection at low cost [8]. The evolution of CMOS technologies and the improvement of circuit techniques led to the success of CMOS chips in the delicate biomedical arena and a wide variety of CMOS-based diagnosis platforms for *in vitro* biomolecule detection based on different transduction mechanisms becomes a reality [9]. For instance, a CMOS fluorimeter detects fluorescent light from labeled biomolecules [10]. Upon the excitation by an external light source, the fluorescent dyes such as Cy3 and Cy5 emit signals for detection by the CMOS chip, yet the labeling process and external modules (i.e., optical filter and excitation light source) confound the operations and degrade its portability. Alternatively, the electrical properties such as impedance [11] or charge flows [12], [13] of the samples can reliably reflect the biological reactions. Their direct and label-free detections circumvent from experimental uncertainty attributed to the perplexity of label on the target, thus reducing the time and labor effort. These electrical-based transducers ineluctably include a recognition probe on the electrode to selectively detect the target of interest. When the target analytes bind with the recognition probe, the transducers convert this event into a measurable electrical signal. The implementation of the recognition layer, however, requires certain chemical post-processing steps, thereby stiffening the preparation step for

the assay. Instead, by employing bio-compatible magnetic particles, CMOS chips are also capable of detecting the targets inside the samples by magnetic-sensitive transducers such as Hall sensors [14] or Giant magnetic resistive spin-valve sensors [15]. As their magnetic particles are labeled on the probe, this quasi labeling-free sensing scheme embellishes the standard surface modification procedure of target labeling [16], simplifying the sample preparation. Compared with the impedometry, the post-processing step on the CMOS chip can be relieved as direct contact between the sample and CMOS chip is averted. However, recognition elements are still entailed to immobilize on a surface in proximity of the transducer for high sensitivity. Therefore, the desired sensing scheme should entail neither complex sample preparation nor chemical postprocessing steps on CMOS chip to achieve rapid and low-cost target detection.

Nuclear magnetic resonance (NMR) is powerful to explore the sample information at the molecular level. The underpinning physics of NMR is the exchange of energy between the RF magnetic field and spin of the non-zero spin nuclei. By exploiting functionalized magnetic nanoparticles (MNPs) as the probe, the NMR-based quasi label-free detection scheme can pinpoint a broad range of unprocessed biological targets such as DNA [17] and protein [18]. Although NMR is known for its relatively low sensitivity, the MNP here provides inherent signal amplification to NMR since a single MNP cluster can affect billions adjacent water molecules [19]. Furthermore, unlike the aforesaid sensing schemes, screening by NMR is rapid and low-cost as it is quasi label-free for the samples and immobilization-free for the transducers/electrodes. Such benefits render NMR-based detection as a promising solution for PoU applications. Recently, researchers have been focusing on miniaturizing the magnet of the NMR and migrating the modular and complex electronics into CMOS chips [20]–[23]. These pathbreaking works significantly shrink the benchtop NMR equipment to a portable scale. However, ascribed to the thermal instability, the magnetic (B)-field variation of the magnet can easily paralyze the NMR experiment. This instability calls for a calibration scheme to enhance the robustness of the system.

Herein, we report a handheld high-sensitivity micro-NMR CMOS platform utilizing a portable magnet (Fig. 1). A Hall sensor with low-noise readout circuit is embedded, for the first time in the literature, with a CMOS micro-NMR transceiver (TRX) to achieve better robustness. The current driver stabilizes the B-field of the magnet against ambient variation. The stabilized B-field avoids the need of a frequency synthesizer to tune the local oscillator (LO), and an untuned LO can be generated directly by the crystal oscillator (XO). The samples under assay are loaded on the on-chip planar coil by a handheld pipette. The sensing coil also serves as a sample heater for thermal profiling. Further, benefitting from the versatility of the NMR assays, this handheld tool unifies multi-type assays such as target detection, protein state analysis, and solvent-polymer dynamics, rendering it suitable for healthcare, food industry and colloidal applications.

This work was presented in *IEEE ISSCC 2016* [24]. Here, we disclose the entire design and engineering of the platform.

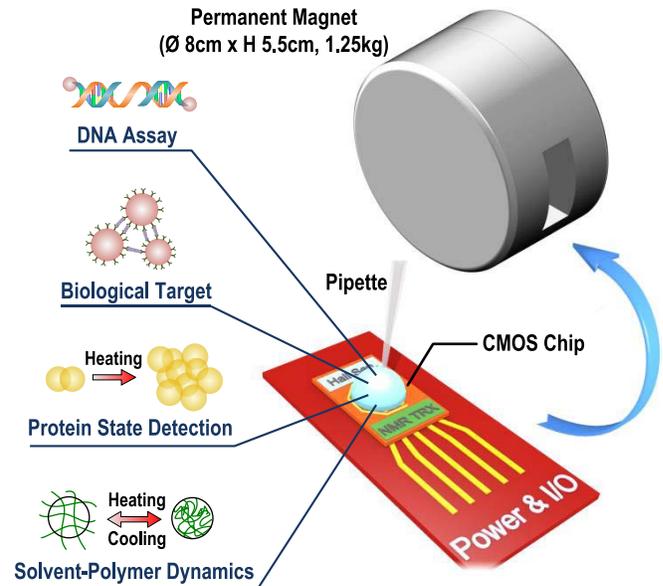


Fig. 1. Conceptual diagram of the proposed micro-NMR platform for PoU applications. Different samples such as protein and DNA can be directly put atop the CMOS chip for assays. A portable magnet is entailed to magnetize the nuclei inside the samples.

Section II couches the challenges of the micro-NMR system and shows the system overview. Section III covers the design of the micro-NMR TRX, the Hall sensor, and its readout circuit. Section IV reveals the measurements and biomolecule experimental results, together with the benchmark with other CMOS bio-sensing platforms. Finally, Section V concludes the paper.

II. CHALLENGES AND SYSTEM OVERVIEW

It is worth discussing the key challenges of micro-NMR systems before introducing our micro-NMR CMOS platform. Relevant books [25] contain the detailed physics of the NMR. Micro-NMR relaxometry (i.e., measurement of the relaxation time of the samples) detects the spin-spin relaxation time (T_2) for the nonzero spin nuclei (i.e., ^1H , ^{13}C or ^{31}P). The nuclei magnetized by the static magnetic field (B_0) absorb an orthogonal RF exciting magnetic field (B_1) at Larmor frequency, $f_L = \gamma B_0$, with the nucleus gyromagnetic ratio γ , and precess about the direction of magnetization at f_L even after the cessation of the excitation. However, the frequency deviation between the exciting pulse frequency (f_{EXC}) and f_L could paralyze the NMR experiment if f_L shifts away from the excitation pulses spectrum. Unlike superconducting magnets that can generate a stable B_0 -field, small permanent magnets suffer from B_0 -field variation due to temperature drift and displacement. For instance, the B_0 -field from a Neodymium magnet can have a temperature coefficient of -1200 ppm/K. As such, f_L of the nuclei shifts together with B_0 , imposing that calibration is essential to safeguard the system against environmental changes. Conventional frequency stabilization techniques are based on the measured NMR signals [22], [26], [27]. However, if the magnetic field fluctuates sufficiently large such that the excitation pulses cannot excite the nucleus effectively, those calibration schemes may not work properly. Previously, we reported a calibration scheme to detect the ambient temperature and then tune the LO

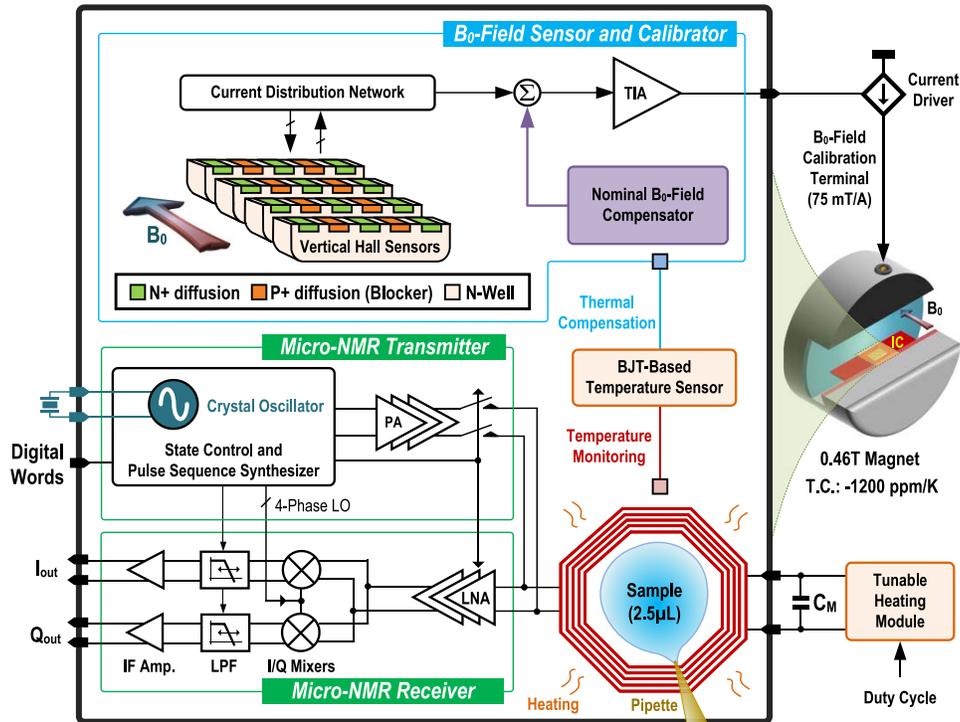


Fig. 2. System block diagram. The transmitter and receiver transduce between magnetic and electrical signals with a thermal-controlled spiral coil. The B_0 -field sensor and calibrator automatically stabilize the bulk magnetization on the μL -sample. No frequency synthesizer is required.

accordingly [28]. However, this method suffers from a delay between the ambient temperature variation and the response on the magnet due to its high heat capacity (time constant > 10 min). Moreover, generating a tunable LO complicates the design of the platform.

A small permanent magnet also poses other challenges to the design of the TRX. As the SNR of the NMR experiment is commensurate with B_0 (i.e., $\text{Signal} \propto B_0^2$), the utilization of the small magnet demands a receiver (RX) with better sensitivity to counteract this signal loss. Additionally, the inhomogeneity of B_0 experienced by the sample entails a programmable transmitter (TX) for synthesizing specific pulse sequences to recover the incoherent spinning from the nuclei.

To circumvent the above challenges, the proposed micro-NMR platform (Fig. 2) includes B_0 -field stabilization to enhance the robustness and simplify the hardware. The Hall sensor and readout circuit, together with an off-chip current driver, manage the lateral B_0 -field and stabilize the bulk magnetization and thus f_L of the nuclei. The dynamic B_1 -field transduction between the nuclei and the electronics is based on an on-chip planar coil driven by a TX/RX together with the matching capacitor C_M , to excite/obtain the magnetic signal to/from the droplet samples ($2.5 \mu\text{L}$) normal to the chip surface. Furthermore, the on-chip NMR sensing coil can act as a sample heater for thermal profiling. A BJT-based temperature sensor aids both the Hall sensor thermal correction and thermal profiling of the samples.

III. CIRCUITS DESIGN

A. Micro-NMR Transceiver

The CMOS chip consists of a TRX for micro-NMR measurements. Starting from the TX, it delivers the pulse sequence

to excite the nuclei, comprising an XO, a pulse sequence synthesizer (PSS) and a power amplifier (PA). This work entails an XO with an oscillation frequency $4 \times f_L$ to directly serve as the LO for both the TX and RX. This single-LO architecture eliminates the phase misalignment of the echo response caused by asynchronous LO, thus assuring a harmonized amplitude and phase of the echoes for peak-amplitude extraction. The XO with an inverter-based amplifier and an off-chip crystal (78.5 MHz) features low-power low-noise operation. Together with the proposed B_0 -field stabilization, the TX avoids the need of a frequency synthesizer to generate a variable LO to track the unstable f_L . The signal from the XO is divided by 4 to provide the 4-phase LO for the PSS and mixers in the RX mode. The PSS flexibly generates different kinds of pulse sequences, such as the Carr–Purcell–Meiboom–Gill (CPMG) pulse [29], [30], according to the command from the FPGA (Fig. 3). The PA, based on class-D tapped inverter chains, delivers voltage pulses to the planar coil to excite the nuclei for the micro-NMR assay.

The generated micro-NMR signals are picked up by the RX that amplifies and downconverts them to the baseband. Ascribed to the moderate B_0 from the permanent magnet, the amplitudes of the micro-NMR signal are limited to a μV level. Thus, to assure sensitive detection the RX entails a meticulous design, demanding an input-referred noise down to $1 \text{ nV}/\sqrt{\text{Hz}}$. To address it, a multistage low-noise amplifier (LNA) is employed to head the RX. Its complementary common-source architecture allows current-reuse and gain-doubling (gain: ~ 30 dB per stage). Following the LNA, a double-balanced quadrature mixer driven by the LO downconverts the amplified signals to the baseband. With a Gilbert-cell architecture, this active mixer provides an additional gain

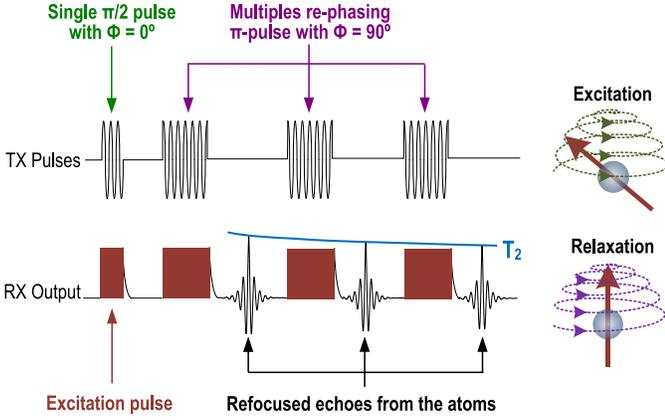


Fig. 3. Timing diagram of the CPMG pulses, including the excitation pulses delivered to the TX to excite the nuclei and the response from the nuclei, which is picked up by the coil. Herein, the single LO architecture for the TRX guarantees in-phase excitation and echoes responses to obtain harmonized peak-amplitudes.

(12 dB) and relaxes the noise requirement for the succeeding stages. After mixing, and to suppress the out-of-band noise and mixing products, a pair of I/Q dynamic-bandwidth low-pass filters (nominal cutoff frequency: 5 kHz) is adopted. Exhibiting a sixth-order Butterworth response and implemented by a cascaded source-follower-based biquad [31], the low-pass filters feature area- and power-efficient operation (FoM = 128 fJ). The cutoff frequency is extended to 20 kHz during the TX mode for fast recovery from saturation after the excitation pulses [23]. This technique shortens the dead-time and strengthens the RX sensitivity [32]. The entire TRX, except the PSS, is differential to improve the immunity to external interferences.

B. Multifunctional Planar Coil

An on-chip planar coil (23 turns, conductor width: $40\ \mu\text{m}$, spacing: $1.85\ \mu\text{m}$) transduces between magnetic signals from the nucleus spin in the axial direction and the electric signals of the TRX. This tiny coil miniaturizes the platform and sample consumption. To improve the quality factor of the coil (simulated $Q = 1.9$ at 19.6 MHz), a thick top metal ($2.5\ \mu\text{m}$) and a stacked-metal-layer technique are applied. The coil in parallel with a capacitor forms an LC tank offering passive amplification for the signal (gain: $\sqrt{Q^2 + 1}$) [21]. Furthermore, to extend the functionality of this micro-NMR platform, the NMR-sensing coil is time-multiplexed to serve as a sample heater for thermal profiling. Modeled in COMSOL Multiphysics, the entire temperature of the droplet uniformly raises ($T < 0.3\ \text{C}$) according to the applied current on the coil [Fig. 4(a)]. The heating temperature is easily controllable by tuning the duty-cycle of the injected current [Fig. 4(b)]. A BJT-based temperature sensor monitors the sample temperature. It has a static temperature offset when reading the droplet temperature due to the physical separation, but the relative value adequately indicates the droplet temperature rise/drop activities. The design temperature rise ($< 20\ \text{C}$) of the IC has negligible effect on the performance of the TRX, as verified by simulations.

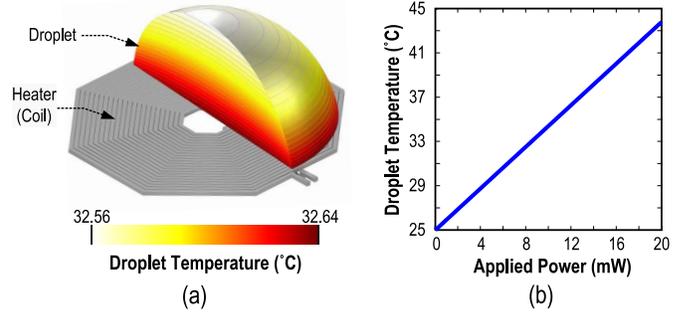


Fig. 4. (a) Simulated 3-D temperature distribution of the droplet at applied power of 8 mW in COMSOL Multiphysics. (b) Simulated droplet average temperature at applied power from 0 to 20 mW.

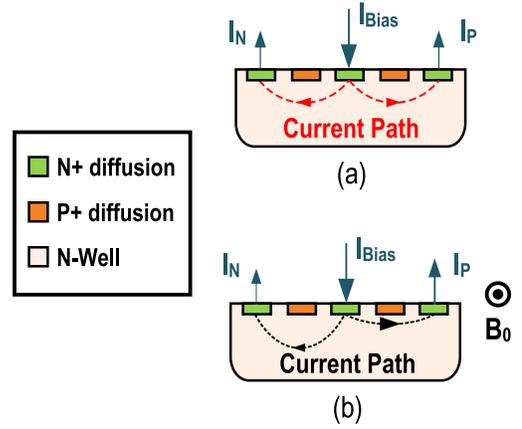


Fig. 5. Cross section of a single VHS element and its current path. (a) Without lateral magnetic field. (b) With lateral magnetic field B_0 .

C. Hall Sensor, Readout Circuit, and Current Driver

The micro-NMR TRX integrates a Hall sensor to sense the B_0 -field variation of the permanent magnet, which must appear orthogonally with the RF magnetic field. The detected B_0 -field variation modulates the current passing through the auxiliary coil of the magnet to stabilize the resultant magnetic field, which allows system-level calibration.

Hall sensors can detect the magnetic field normal [33]–[35] or parallel [36], [37] to the chip surface. As the RF magnetic field generated by the planar coil is normal to the chip surface, the latter solution is required, which can be achieved by a vertical Hall sensor (VHS). Each VHS subelement contains an n-well as the substrate, and three N-diffusions as contacts [Fig. 5(a)]. P-diffusions are embedded between the n-diffusions to avert the current flowing at the surface, soothing the $1/f$ noise. This architecture renders the device with full standard CMOS compatibility. Working in the current mode implies that the input is a constant current source (I_{Bias}) and outputs are two current terminals (I_P and I_N). When no external magnetic field exists, there is an equal split of I_{Bias} between I_P and I_N , which should be equal with no current difference.

When considering B_0 in the lateral direction (i.e., nominal to the cross section of the VHS device), the charge carriers from the input terminal will experience a Lorentz force deflecting

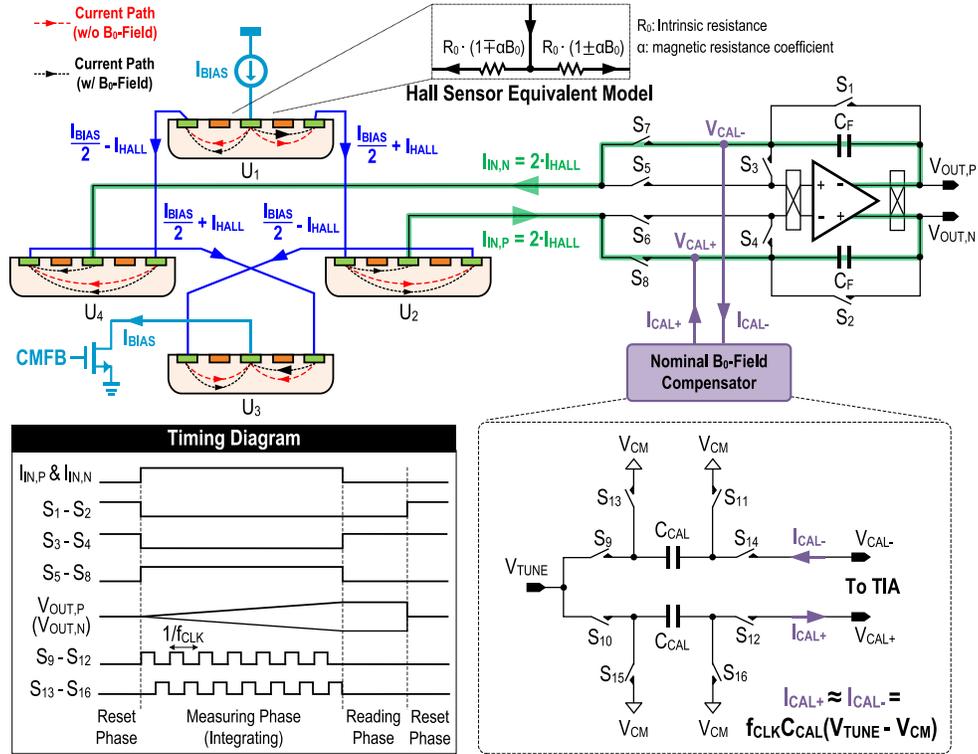


Fig. 6. Proposed current-mode 4-folded VHS arranged in Wheatstone bridge to sense the lateral B_0 -field and its readout circuit (spinning circuitry is omitted for simplicity). The latter features a nominal B_0 -field compensator to offset the strong nominal B_0 -field (0.46 T) for better sensitivity (3.75 mT). The green arrows highlight the current paths of I_{Hall} . Inset shows the timing diagram for the switches and overall operations.

the charges, leading to nonidentical magnitudes of I_P and I_N [Fig. 5(b)] expressed by

$$I_P = \frac{I_{Bias}}{2} + I_{Hall}(B_0) \quad (1)$$

$$I_N = \frac{I_{Bias}}{2} - I_{Hall}(B_0) \quad (2)$$

where $I_{Hall}(B_0)$ is the induced Hall current on each output terminal commensurate with B_0 . Thus, the measurement of I_{Hall} can determine B_0 . However, the prodigious bias component I_{Bias} stiffens the measurement on I_P and I_N . To circumvent this measurement barrier, four identical VHS subelements U_{1-4} are arranged to form a Wheatstone bridge (Fig. 6). The Wheatstone bridge prunes I_{Bias} at the output and only the B_0 -dependent term I_{Hall} appears at the Wheatstone bridge's output. Furthermore, this configuration not only features a fully-differential architecture, but also doubles the output Hall current improving the sensitivity of the VHS. A common-mode feedback circuit regulates the tail of the Wheatstone bridge, where two-phase spinning eliminates the effect of mismatch between the VHS subelements by periodically interchanging the output and supply terminals of the Wheatstone bridge (Fig. 7).

The induced differential current from the Wheatstone bridge ($I_{IN,P}$ and $I_{IN,N}$) is then converted to voltage for recording. Among multifarious options of transimpedance amplifiers (TIAs), a current integrator formed by a high-gain amplifier with shunt integrating capacitor (C_F) appears as a promising solution, since it inherently offers low-pass filtering on the outputs without passive noise sources (e.g., feedback

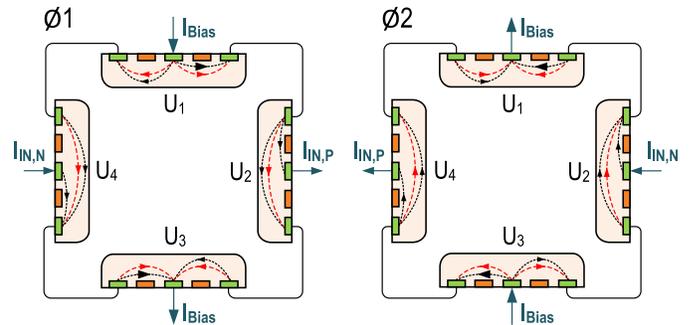


Fig. 7. Diagram of the two-phase spinning technique on the VHS. The bias direction (U_1 and U_3) together with the output terminals (U_2 and U_4) of the VHS are swapped periodically to eliminate the $1/f$ noise and offset of the elements.

resistors for resistive feedback TIA), leading to a better noise performance [38]–[40]. Furthermore, the variation of the integration time T_{INT} will alter the gain of the fully differential TIA, providing conversion flexibility (Fig. 8). The core of this TIA is a two-stage amplifier with a telescopic 1st stage, where the DC gain (A_{DC} , 100 dB) guarantees accurate and stable operation ($GBW = 100$ MHz, $PM = 50^\circ$ at a 15 pF load), at a power budget of 2 mW excluding the bias circuit. A chopper deals with the offset and $1/f$ noise of the amplifier with a chopping frequency of 1 MHz. This chopping technique reduces the $1/f$ noise corner by 10000 \times (from 200 kHz to 20 Hz) in an open-loop configuration. During the reset phase, S_{1-4} nullify the residual voltages on C_F , while opening S_{5-8} prevents the current to flow from the Wheatstone

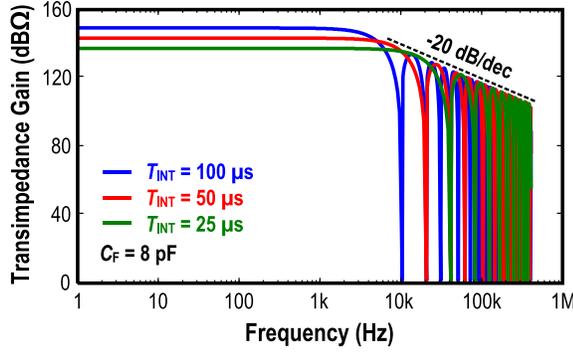


Fig. 8. Simulated frequency response of the TIA with various T_{INT} .

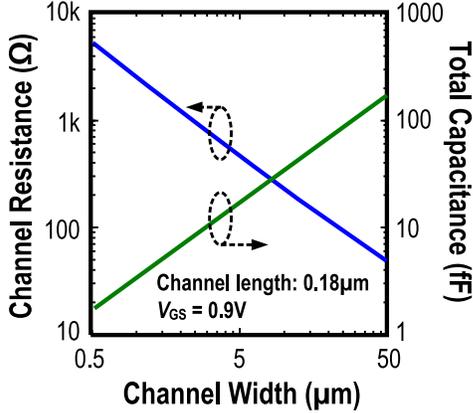


Fig. 9. Simulated channel resistance (R_{DS}) and parasitic capacitance ($C_G + C_D$) of the N-channel MOSFET versus channel width.

bridge into the TIA. In the measuring phase $I_{IN,P}$ and $I_{IN,N}$ flow through S_{7-8} and charge C_F , causing a complementary voltage ramp at the output of the TIA, expressed by (assuming an ideal TIA)

$$V_{OUT} = V_{OUT,P} - V_{OUT,N} = \frac{4I_{Hall}T_{INT}}{C_F} \quad (3)$$

For an ideal TIA, its input resistance ($R_{IN,TIA}$) should be zero to measure its input current accurately. The finite gain of the amplifier and the intrinsic resistances of the switches (R_{SW}), however, ineluctably exacerbate the performance of the TIA. Considering a TIA with n switches to regulate the current flow from the Wheatstone bridge and the spinning circuitry in its front, the average $R_{IN,TIA}$ below the resetting frequency ($1/T_{INT}$) should be

$$R_{IN,TIA,conv} = \frac{T_{INT}}{A_{DC}C_F} + n \cdot R_{SW} \quad (4)$$

assuming a negligible reset time relative to T_{INT} . The first term on the right-hand side of (4) is the input impedance of the integrator whereas the second term emerges from the switches. While a multistage architecture can boost the gain of the amplifier at the expense of power consumption [41], the value of R_{SW} is limited to a certain range in a particular process since a large switch inextricably induces parasitic capacitance on the signal paths (Fig. 9). To this end, it is mandatory to diminish the effect of R_{SW} on the TIA. To achieve this, S_{7-8} are managed to guide the current passing through the feedback paths, while avoiding switches between the TIA

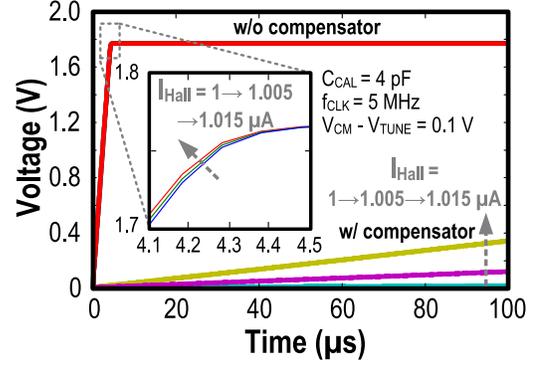


Fig. 10. Simulated output waveforms of the integrator. Without the nominal B_0 -field compensator, the integrator is saturated due to the large current induced by the nominal B_0 -field before it accumulates an adequate voltage difference, whereas, with the compensator, the nominal B_0 -field can be compensated, thus the integration time can be prolonged to produce sufficient voltage differences at the output.

and Wheatstone bridge. S_{5-6} provide connections between the input of the amplifier and the Wheatstone bridge. As long as there is no current (except leakage currents) flowing through S_{5-6} , the operation of the TIA will not be deteriorated. $R_{IN,TIA}$ in the proposed architecture is given by

$$R_{IN,TIA,prop} = \frac{T_{INT}}{A_{DC}C_F} + \frac{n \cdot R_{SW}}{A_{DC}} \quad (5)$$

In this switching scheme, A_{DC} represses the impact of the switches and allows the realization of a low-input-impedance TIA without creating a large parasitic capacitance at the signal paths. For example, $R_{IN,TIA,prop} = 171 \Omega$ (simulated) with $C_F = 8 \text{ pF}$, $T_{INT} = 100 \mu\text{s}$ (gain: $250 \text{ V}/\mu\text{A}$) and $R_{SW} = 240 \Omega$. When compared with the general approach that places four switches (two for mastering and two for spinning) before the TIA [34], the suppression of $R_{IN,TIA}$ is now 85%, while the TIA absorbs $\sim 20\%$ more current (output impedance from Wheatstone bridge: $5 \text{ k}\Omega$). The input-referred noise of the TIA is $10 \text{ pA}/\sqrt{\text{Hz}}$ at 1 Hz , which is $79\times$ better than the case without the chopper. After the measuring phase, the Hall currents from the Wheatstone bridge cease by switching off S_{5-8} , while turning on S_{3-4} allows the reading of the voltage on C_F . The three-phase operation can be repeated and the results can be averaged to reduce the background noise.

The extension of T_{INT} produces an acceptable output level (e.g., LSB of the external ADC: 0.8 mV) induced by the B_0 -field variation (3.75 mT). However, ascribed to the prodigious nominal B_0 -field, a typical TIA can saturate and fail to sense this tiny B_0 -field variation. The addition of a nominal B_0 -field compensator implemented by a pair of switched-capacitor based current source with alternative phase (i.e., opposite current flow) is necessary to obviate this contrariety. During the measuring phase, the compensator nullifies the Hall current induced from the nominal B_0 -field flowing into the TIA by generating and injecting the currents (I_{CAL+} and I_{CAL-}) into the TIA. Thus, there is only the integration of the variable part from the Hall sensors at the output of the TIA (Fig. 10), and, by T_{INT} extension, more gain is achieved without saturating the TIA.

The current driver receives the converted results after digitization and subsequently drives the auxiliary coil of the

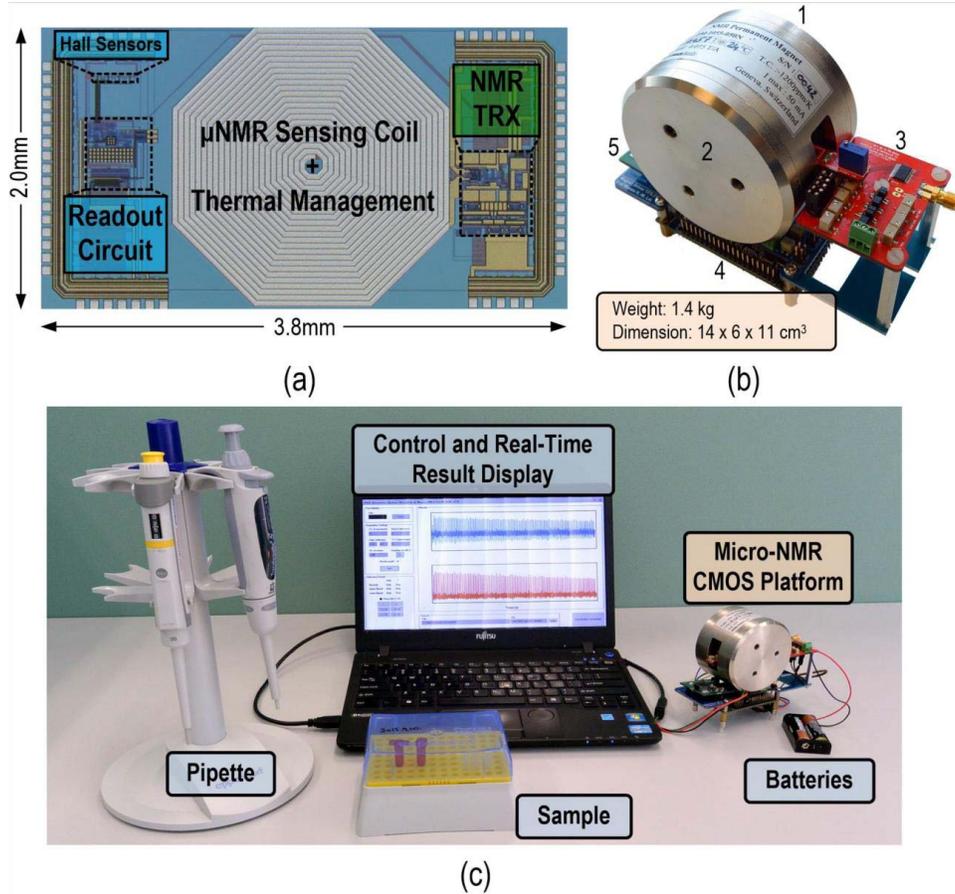


Fig. 11. (a) Chip photograph of the fabricated chip in 0.18 μm CMOS. (b) Prototype of the micro-NMR platform with B_0 -field stabilization and Lab-on-a-chip feasibility for multi-type biological/chemical assays, including: 1) permanent magnet; 2) CMOS micro-NMR chip (inside magnet); 3) PCB; 4) FPGA and 5) current driver. (c) Experimental setup. A program developed in C# is entailed for hardware control and visualizing the experimental results. The platform is powered by two batteries for portability.

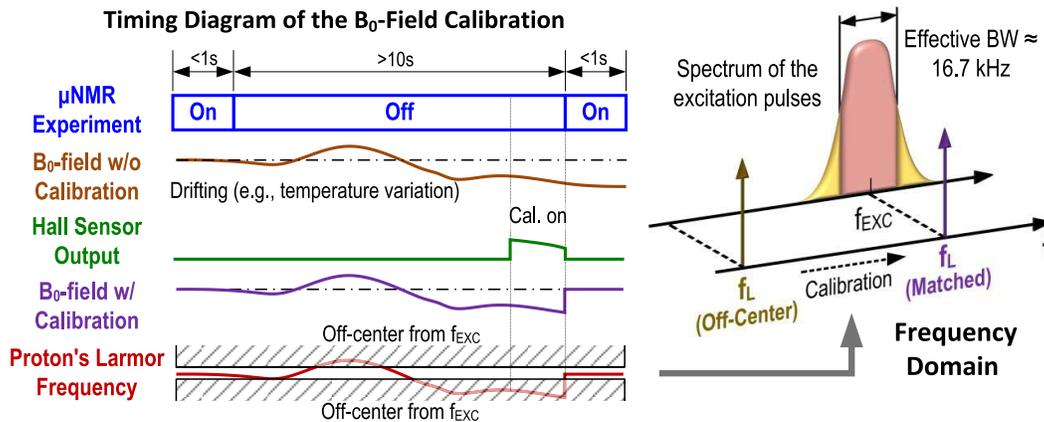


Fig. 12. Timing diagram of the B_0 -field calibration and its frequency-domain illustration.

portable magnet and trims the B_0 -field. Based on the Biot-Savart law, this injected current to the coil generates an additional magnetic field (75 mT/A). This current modulates and stabilizes the actual B_0 -field, which is the sum of the B_0 -field from the permanent magnet and auxiliary coil [42], according to the results from the Hall sensor. The current driver implemented off-chip with discrete components prevents its interference (on-chip) due to the related high power consumption.

IV. EXPERIMENTAL RESULTS

A. Electrical Measurements

Fabricated in the 1P6M 0.18- μm CMOS process, the chip occupies an area of $2.0 \times 3.8 \text{ mm}^2$, dominated by the planar coil (dimension: $2.0 \times 2.0 \text{ mm}^2$, L : 506 nH, Q : 1.84) as shown in Fig. 11(a). The dimension ($14 \times 6 \times 11 \text{ cm}^3$) and weight (1.4 kg) of the system are dominated by the 0.46-T portable magnet (weight: 1.25 kg, diameter: 8 cm,

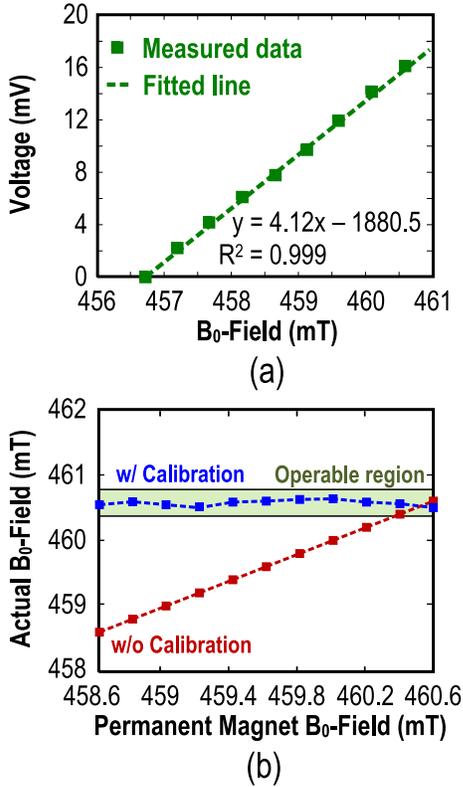


Fig. 13. (a) Measured Hall sensor response. (b) B₀-field with and without calibration. Actual B₀-field is the sum of the B₀-field from the permanent magnet and the auxiliary coil driven by the current driver.

height: 5.5 cm). In addition to the CMOS chip, there are a system PCB, a commercial FPGA (DE0-Nano) and a current driver [Fig. 11(b)]. A customized program on the PC controls the platform, simplifying the operation and visualization of the assay results [Fig. 11(c)]. The computer extracts and evaluates the T_2 of the samples from the acquired signals. During the micro-NMR experiments, the RX mode consumes 22.1 mW dominated by the forefront LNA for high sensitivity. For the TX mode, the power consumption is 51.0 mW dominated by the PA that has a power efficiency of 31.6%. The VHS together with the readout circuit draw 8.4 mW. Both the TRX and Hall sensor parts switch into the idle mode after each assay to prolong the lifetime of the battery. With 2 AA batteries of 2.5 Wh capacity and a DC-DC converter efficiency of 90%, the system can finish up to 500000 assays, assuming that the VHS is on for 1 s, then the NMR RX is on for 1 s (TX pulses are short such that the power of TX can be neglected in this case), and finally the entire system is idle for 9 s.

Before each micro-NMR assay, the Hall sensor is turned on first with the VHS sensing the B₀-field (Fig. 12). As the B₀-field may shift away from its nominal value due to environmental variation (e.g., temperature and sample-to-magnet position), an untracked f_L can be easily off-center from f_{EXC} (BW: 16.7 kHz, equivalent to 0.5 mT in terms of B-field) and the operation of the platform can be paralyzed. With the proposed calibration scheme, the B₀-field variation is tracked by the VHS and readout circuit; they show a sensitivity of 4.12 V/T [Fig. 13(a)]. The eventual B₀-field is then balanced by modulating the auxiliary coil of the magnet with a particular

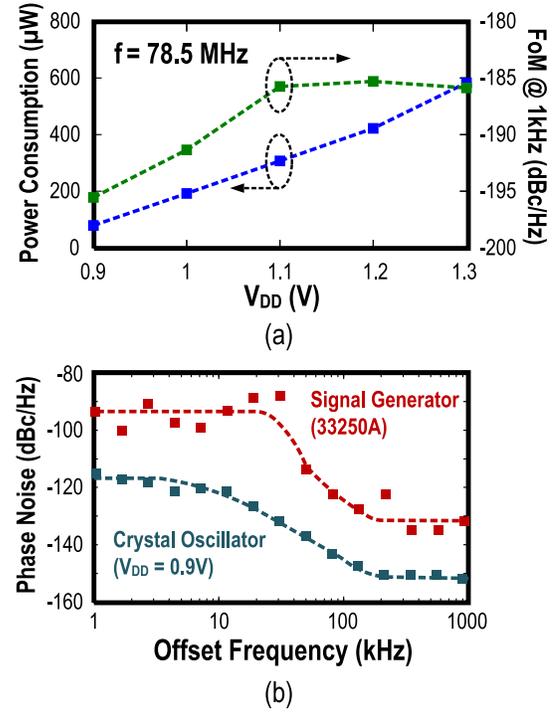


Fig. 14. (a) Measured power consumption and FoM of the crystal oscillator at different supply voltages. (b) Measured phase noise of the crystal oscillator ($V_{DD} = 0.9$ V, $f = 78.5$ MHz). Compared with the LO generated from signal generator (Agilent 33250A), the crystal oscillator shows a much better phase noise at low power.

magnitude of dc current, according to the result from the VHS and readout circuit. Thus, f_L can be reset to match with f_{EXC} to proceed the micro-NMR assay. Associated with signal-averaging performed in the frequency domain to suppress the background noise on the Hall sensor, the proposed calibration improves the B₀-field stability by 13× (from 2 to 0.15 mT) at 0.46 T nominal B₀-field ($f_L = 19.6$ MHz), corresponding to a variation on f_L of 6.9 kHz [Fig. 13(b)]. This variation is smaller than the BW of the excitation pulse, ensuring a secure micro-NMR operation. While this calibration scheme can handle large magnetic field fluctuation, the calibration for small magnetic field fluctuation such as [27] can also be performed in parallel to enhance the resolution if necessary.

The stabilized f_L allows the use of a simple XO as the LO, which measures low power (79 μW at V_{DD} of 0.9V) and low phase noise (-116 dBc/Hz at 1 kHz offset), as shown in Fig. 14(a) and (b), respectively.

B. Biological/Chemical Measurements

One of the crucial aims of this platform is to detect and quantify the biological target inside the samples for disease screening. For instance, considering human immunoglobulin G (IgG), which protects the body from infections by binding themselves to different pathogens. Protein A, which specifically binds Human IgG, used as a probe to detect the existence of IgG inside the samples by functionalizing them on the water-soluble MNPs ([Fe₂O₃]: 10 μg/mL, Ø: 25-30 nm). These superparamagnetic MNPs have signifi-

TABLE I
SUMMARY AND BENCHMARK WITH OTHER CMOS-BASED PoU SYSTEMS

	Sensing mechanism	No. of sensors	Demonstrated detection	Probe immobilization	Labeling	Technology (Chip area)
This work	NMR relaxometry	1	DNA and protein detection	No	Quasi label-free	0.18 μm (7.6 mm^2)
[10]	Fluorescent-sensing	56	DNA detection	Yes	Cy3-label	0.35 μm (9.0 mm^2)
[11]	Capacitive-sensing	54	DNA detection	Yes	Label-free	0.35 μm (20.0 mm^2)
[12]	Cyclic voltammetry	12	DNA detection	Yes	Label-free	0.13 μm (9.0 mm^2)
[14]	Magnetic-sensing	64	Protein detection	Yes	Quasi label-free	0.35 μm (8.9 mm^2)
[15]*	Magnetic-sensing	256	Protein detection	Yes	Quasi label-free	0.18 μm (7.29 mm^2)
[20]*	NMR relaxometry	1	Protein detection	No	Quasi label-free	0.18 μm (3.8 mm^2)
[21]	NMR relaxometry	1	Protein and cell detection	No	Quasi label-free	0.18 μm (11.3 mm^2)

*Off-chip sensors

the MNP. By optimizing these parameters, the detection limit of the micro-NMR platform can be further decreased to a fM level [43].

One of the unique features of the NMR is its capability to probe the molecular structure of the samples, which renders this micro-NMR platform a versatile tool for different areas of application. For instance, protein β -lactoglobulin (β -LG), which is a major protein found in milk products, has significant impacts on the texture as well as nutritional quality of the food [44]. β -LG exists as a native dimer at room temperature. When they experience heat > 60 °C, β -LG denatures and aggregates as globules irreversibly. This state transformation decreases T_2 of the sample as the transverse relaxation of protons is sensitive to the aggregation. In fact, T_2 from the samples drops from 40% to 74% when heated to 95 C according to their concentration [Fig. 15(c)]. Thus this T_2 deviation can indicate the state of the whey proteins of foods.

Similarly, the adoption of this platform allows the detection of the dynamics between the solvent and polymer for colloidal application. Poly(N-isopropylacrylamide) (PNIPAM), widely used as the advanced sensor and drug delivery carrier, demonstrates temperature-induced volume phase transition at lower critical solution temperature (~ 33 °C) in the water [45]. This phase transition affects the local environment in terms of solvent confinement and subsequently T_2 of the solvent. To diversify the capabilities of this platform, the NMR sensing coil is utilized as a thermal heater to perform thermal profiling on the PNIPAM. When the applied power to the heater is small (duty cycle $\leq 2\%$), the temperature of the sample does not rise, and the PNIPAM stays in a swollen state. If the power is increased gradually (duty cycle $\geq 4\%$), the PNIPAM starts to undergo a volume phase transition. This collapsed state of PNIPAM confounds the mobility of bound water molecule then resulting in T_2 decrement of the solvent ($\sim 9\%$) [Fig. 15(d)]. The ability

of sample thermal control is predominantly limited by the sample evaporation. In fact, adding a PDMS mold can prevent the sample evaporation and further increase the ability to heat the sample.

C. Comparison and Discussion

Table I compares this work with recent CMOS PoU tools. It supports multi-type assays in one unified platform, while achieving high sensitivity and selectivity for DNA, as well as other proteins targeting capability inside 2.5 μL sample with corresponding probe-decorated MNPs. The NMR-based sensing scheme eases the hardware and sample preparation for the assays (i.e., post-processing and probe-immobilization on CMOS, tag labelling on target, washing of magnetic particles) when compared to others such as fluorescent/capacitive-sensing. While compared with the previous reported CMOS NMR systems, the B_0 -field stabilization module here facilitates the electronics, and enhances the robustness of the platform (Table II). Moreover, this micro-NMR platform includes a thermal module, which is an added feature to explore the thermal profile of the samples and opens up a broad range of possibilities for on-chip thermal analysis [46], [47]. However, due to the large sensing coil, the implementation of microarray for multiplex sensing is unfeasible. In addition, limited by the homogeneity of the magnet, NMR spectroscopy, as demonstrated by Ha *et al* [22], is not supported by this platform. Still, the NMR relaxometry applications demonstrated here render this platform as a promising candidate for PoU tool. A comparison with a commercial benchtop relaxometer shows that this platform consumes 120 \times fewer samples and is 96 \times lighter, 175 \times smaller, and 16 \times cheaper when evaluated against the selling price of [48], with a tradeoff of lower tuning range on sample temperature. Together with the proposed

TABLE II
BENCHMARK WITH PREVIOUS CMOS NMR SYSTEMS

		This Work	JSSC'09 [20]*	JSSC'11 [21]
Application	Specificity	1. Target detection 2. Solvent-polymer dynamics 3. Protein state analysis	Target detection	Target detection
	Demo target	DNA/Human IgG	Avidin	hCG cancer marker/ Bladder cancer cell
	Detection limit	<100 pM# (DNA)	5 nM (Avidin)	5 nM (Cancer marker)
	Sample handling limit	2.5 μ L	5.0 μ L	5.0 μ L
Hardware	Physics	NMR relaxometry + Thermal management	NMR relaxometry	NMR relaxometry
	LO generation	Crystal oscillator (Off-chip crystal)	On-chip/Off-chip	Off-chip
	B_0 -field calibration	Yes	No	No
	CMOS tech.	0.18 μ m	0.18 μ m	0.18 μ m
	Chip area	7.6 mm ²	3.8 mm ²	11.3 mm ²

*Off-chip sensors

#50 pM reported in [24] with 95 % confidence level.

B_0 -field stabilization scheme, that facilitates the operations, this system is in the vanguard of CMOS PoU tools.

V. CONCLUSION

By incorporating the Hall sensor and micro-NMR TRX, this paper described a handheld CMOS micro-NMR platform with effective closed-loop B_0 -field stabilization, and evinced superiority of the NMR-based sensing for PoU assay from different points-of-view. In terms of applications this platform exhibited the utilization of a single CMOS chip to attain versatile sensitive chemical/biological assay from diverse unprocessed samples such as protein, DNA, and polymer. The LoD of the platform for *Enterococcus faecalis* derived DNA is <100 pM from a 2.5 μ L sample using functionalized MNPs. The developed thermal module also extends the potential applications (e.g., polymer dynamics study). In terms of hardware, the micro-NMR platform reveals high robustness confirmed by the exploration of the current-mode Hall sensor and readout circuit in conjunction with a current driver to stabilize the B_0 -field variation attributed to the environments. We anticipate that this handheld micro-NMR platform will help to popularize the application of NMR and magnify the advantages of NMR to our daily lives.

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348 publications. He has also coauthored seven U.S. Patents. He has created the *Analog and Mixed-Signal VLSI Research Laboratory of UM*: http://www.fst.umac.mo/en/lab/ans_vlsi/website/index.html, elevated in January 2011 to State Key Lab of China (the first in Engineering in Macao), being its Founding Director.

Prof. Rui Martins was the Founding Chairman of the IEEE Macau Section from 2003 to 2005, and of the IEEE Macau Joint-Chapter on Circuits And Systems (CAS)/Communications (COM) from 2005 to 2008 [2009 World Chapter of the Year of the IEEE Circuits And Systems Society (CASS)]. He was the General Chair of the 2008 IEEE Asia-Pacific Conference on Circuits And Systems - *APCCAS'2008*, and was the Vice-President for the Region 10 (Asia, Australia, the Pacific) of the IEEE Circuits and Systems (CAS) Society, for the period of 2009 to 2011. He is now the Vice-President (World) Regional Activities and Membership also of the IEEE CAS Society for the period 2012 to 2013. He was an associate editor of the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS from 2010 to 2013. He is also a member of the IEEE CAS Society Fellow Evaluation Committee (Class of 2013). He was the recipient of two government decorations: the Medal of Professional Merit from Macao Government (Portuguese Administration) in 1999, and the Honorary Title of Value from Macao SAR Government (Chinese Administration) in 2001. In July 2010 was elected, unanimously, as Corresponding Member of the Portuguese Academy of Sciences (in Lisbon), being the only Portuguese Academician living in Asia.