

澳門大學

UNIVERSIDADE DE MACAU

UNIVERSITY OF MACAU

Major Programme:	Master	of Sci	ence ir	n Micr	oelectr	onics	& Mas	ter of I	Philoso	ophy	in Micr	oelectr	onics				
Course Type:	$\begin{tabular}{lllllllllllllllllllllllllllllllllll$																
Course Title: (in Chinese and English)	Power l 電源管	Manag 理集成	ement ] え 電路 診	Integra 殳計	grated Circuit Design				Suggested Year of Study:			Year 1					
Duration:	Semester Course				Yearly Course				Credit Units:			3					
Grading System:	ing System: 🗹 Letter Grade				D P/NP				Pre-requisite: (if any)			None					
Medium of Instruction:				English													
Course Description:	This course starts with a single transistor, with introductions on transistor fabrication and elect characteristics. Then, a single-stage analog amplifier will be analyzed, design and analyses of a two-s amplifier will be also discussed. After we know the analog integrated circuit (IC) design basics, we learn the basic components of power management ICs: low-dropout regulators, inductor-based switched-capacitor DC-DC converters. Last but not least, wireless power transfer circuits and systems be briefly introduced.											ectrical o-stage ve will ed and ns will					
Intended Learning Outcomes (ILO):	<ul> <li>This course enables students to have:</li> <li>Apply the essential knowledge in power management integrated circuits design.</li> <li>Design and simulate power management ICs using EDA tools with CMOS process.</li> <li>Present the design project results in both orally and in IEEE-style reports.</li> </ul>																
Major Assessment Methods:			Student Presentation	Individual project / paper	Group project / paper	Group discussions	Writing Assignment	Exercises & problems	Service learning	Internship	Field study	Company visits	Reading & Writing Assessments / tests	Listening & Oral Assessments / tests	Others (please specify)		
Class Participation /	%								$\checkmark$								
Assignment(s) 20	%								$\checkmark$								
Test(s)20	%				$\checkmark$												
Examination	%																
Others: Project 50	_%			$\checkmark$		$\checkmark$											
Course Content: (topic outline)	_	- - - -	Introdu Analog PMOS/ Digital PID co Inducto Switch topolog Wireles overvie	n and background, review of analog basics. -dropout regulators: feedback loops, super source follower, flipped-voltage follower, OS regulator. -dropout regulators: digital control loop design, analog-assisted digital control, and analysis for regulators. Ised DC-DC converters: switching power converter topologies, converter analyses apacitor DC-DC converters: charge redistribution loss, switched-capacitor DC-DC loss analyses ower transfer circuits and systems: wireless power transfer applications, system patifier design, output voltage follower, flipped-voltage follower, flipped-voltage follower, flipped-voltage follower, output voltage follower, flipped-voltage follower, flip													