

UNIVERSIDADE DE MACAU UNIVERSITY OF MACAU

Major Programme:	Master	of Scie	ence in	Micro	oelectr _e	onics &	& Mast	ter of I	hilosc	phy 11	n Micro	pelectro	onics				
Course Type:	✓ CM − C☐ RE − Re	Compulso equired E			L&S – Languages and Skills CPE – Community and Peer Education * GE – General Education								ion	_	– Minor – Free Ele	ective	
Course Title: (in Chinese and English)	Microel 電路設		ic Circ	cuit D	t Design 微電子集成				Suggested Year of Study: Year 1								
Duration:	☑ Semester Course				☐ Yearly Course				it Unit	s:	3						
Grading System:	rstem: 🗹 Letter Grade				☐ P/NP				equisit	te:	None						
Medium of Instruction:				Er	English												
Course Description:	This course is designed to discuss on fundamental principles for analysis and design of analog circuits and the practical considerations in integrated circuit design. Students will learn how to design, analyze and evaluate amplifiers as an essential integrated circuit building block. Advanced amplifiers/analog circuit layout techniques with case studies will also be introduced. By providing hands-on practice with one real advanced VLSI CMOS process using industrial EDA tools, students can have a deep understanding on how to design practical integrated circuits and make real world engineering tradeoffs. This course will be assessed with assignments, presentations and projects.																
Intended Learning Outcomes (ILO):	 This course enables students to have: To introduce the essential knowledge in analog circuits design. To introduce common analog circuit building blocks with practical considerations. To teach students with hands on experience on designing and simulating analog circuits using industrial simulation tools with real world CMOS process. 																
Case Study Major Assessment Methods:		Role Playing	Student Presentation	Individual proiect / paper	Group project / paper	Group discussions	Writing Assignment	Exercises & problems	Service learning	Internship	Field study	Company visits	Reading & Writing Assessments / tests	Listening & Oral Assessments / tests	Others (please specify)		
Class Participation / Discussion <u>0</u> %																	
Assignment(s)20	%								√								
Test(s) 0	_%																
Examination 0	_%																
Others: Project 80	_%			V	√	V											
Course Content: (topic outline)		-] -] - (-] - 1	Introduce Basic se MOSFI Current Noise a Amplifi Frequer Two-sta	emicor ET dev mirror nalysis ers ncy res	ices rs and v s ponse	oltage/		referei	ices								

Template revised on 20 Oct 2017